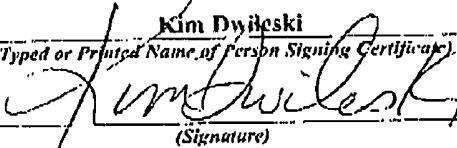


CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)				Docket No. BUR920010100US1
Applicant(s): Joseph A. Iadanza				
Serial No. 09/683,872	Filing Date 2/26/2002	Examiner Ortiz, Edgardo	Group Art Unit 2815	
Invention: METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS				
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Applicant: Joseph A. Iadanza

Group Art Unit: 2815

Filed: 2/26/2002

Examiner: Ortiz, Edgardo

Serial No.: 09/683,872

Title: METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed January 14, 2004, please amend the above-identified application as follows:

09/683,872

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